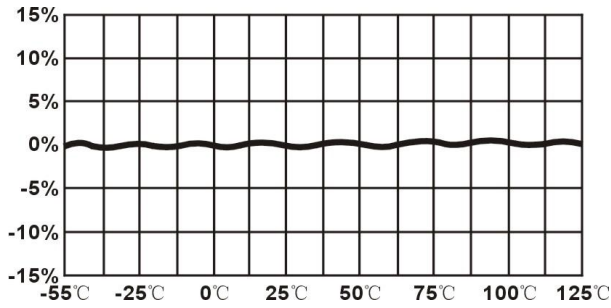
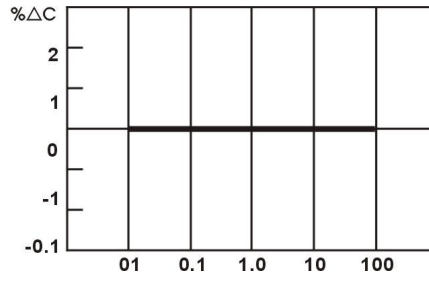


• **NPO**

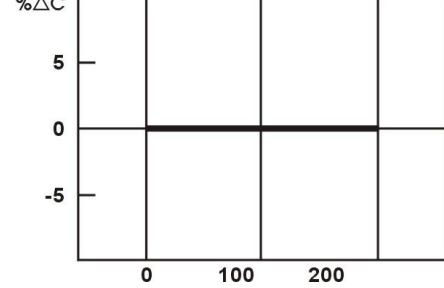
(1) NPO Capacitance Change vs Temperature



(2) Frequency (MHz)

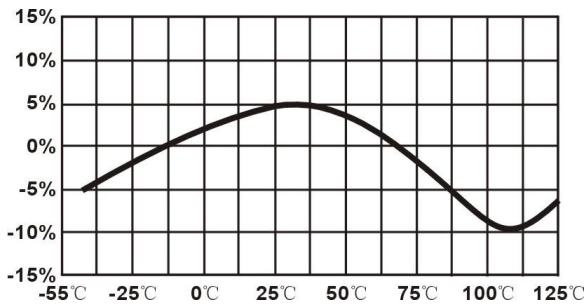


(3) DC voltage (D.C. volt)

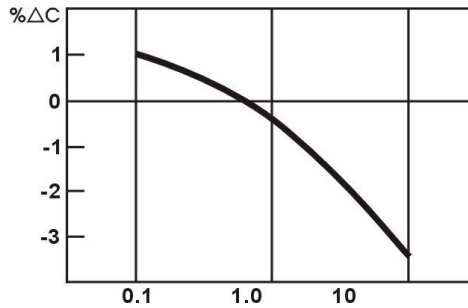


• **X7R**

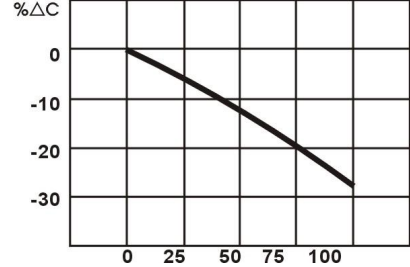
(1) X7R Capacitance Change vs Temperature



(2) Frequency (MHz)

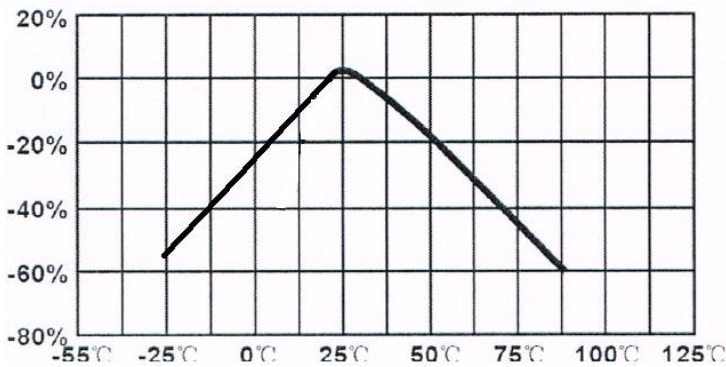


(3) DC voltage (D.C. volt)

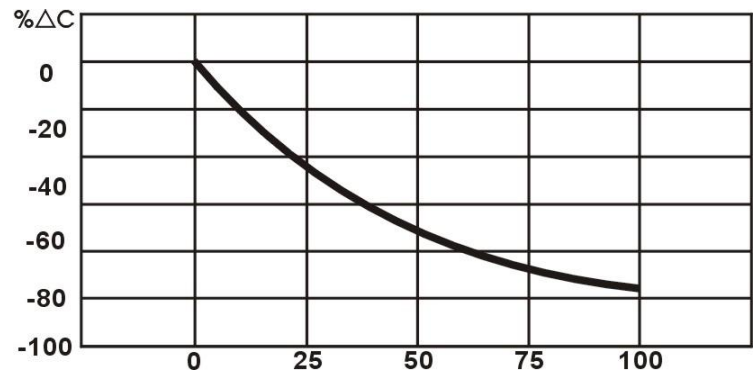


• **Z5U**

(1) Z5U Capacitance Change vs Temperature

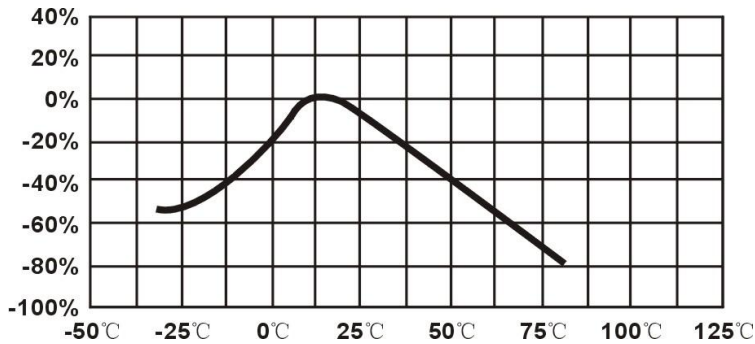


(2) DC voltage

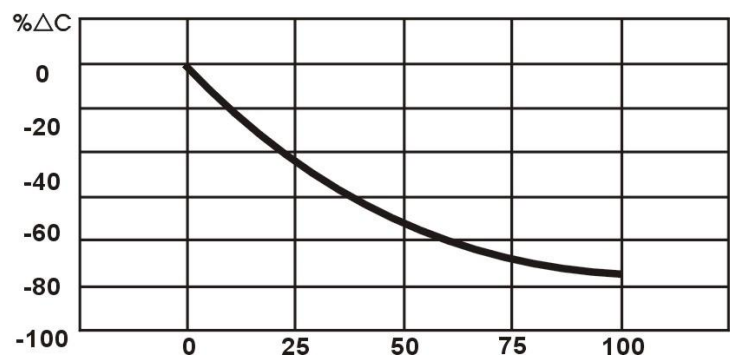


• **Y5V**

(1) Y5V Capacitance Change vs Temperature

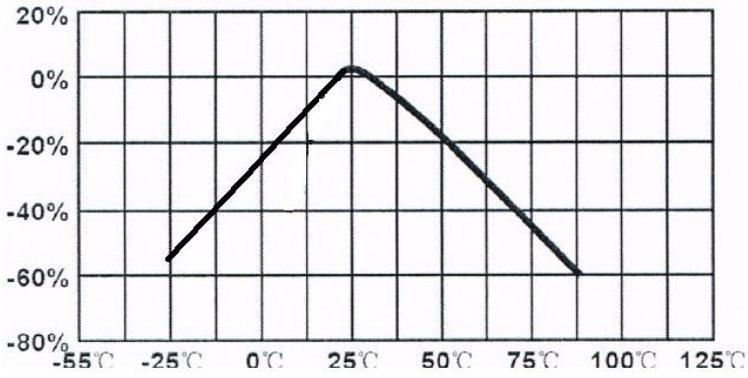


(2) DC voltage



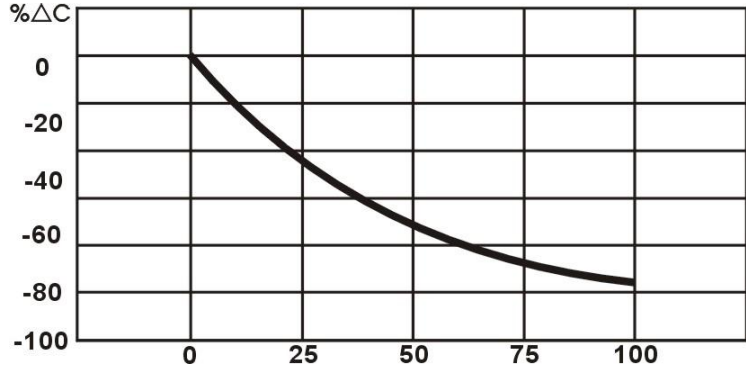
• Y5U

(1) Y5U Capacitance Change vs Temperature



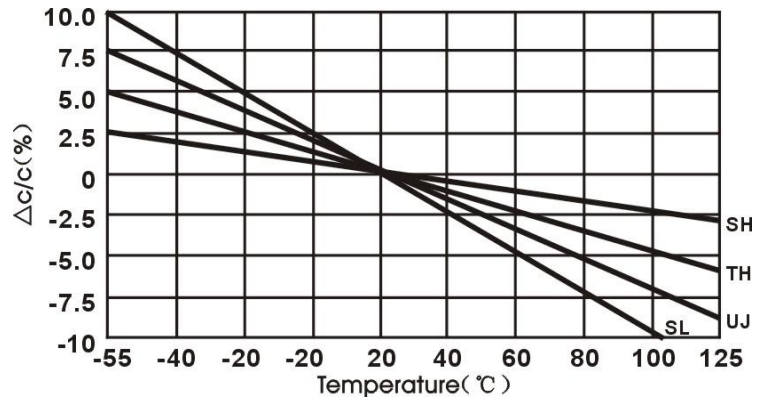
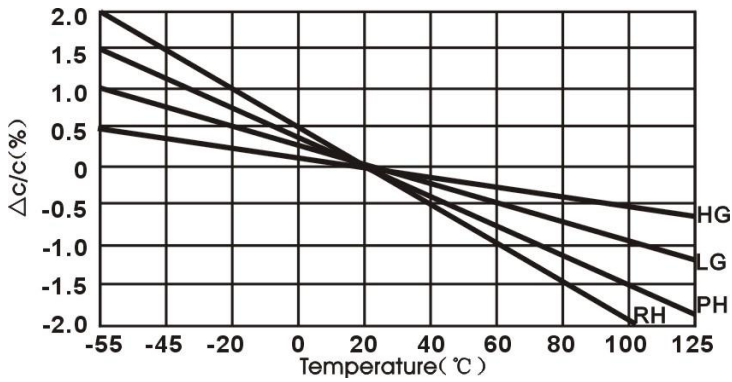
(2) DC voltage

D.C volt



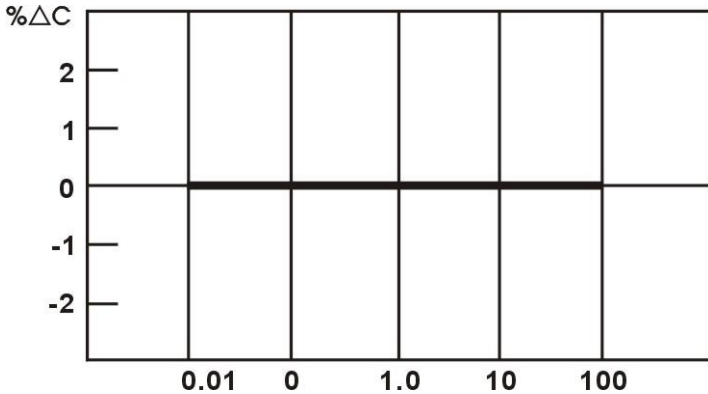
• Temperature Compensating MLCC

(1) Capacitance Change vs Temperature



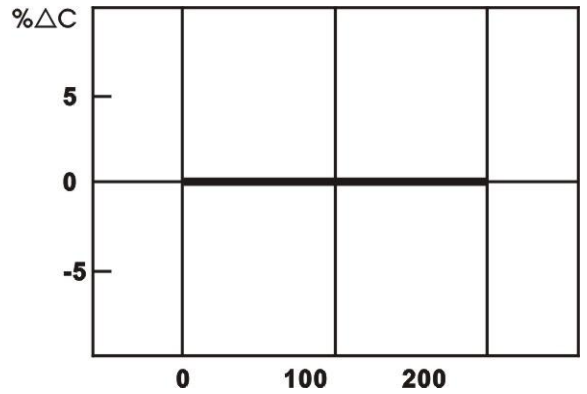
(2) Frequency

(MHz)



(3) DC voltage

D.C volt



Note: Specifications are subject to change without notice. For more detail and update, please visit our website.